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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10041609	FILING DATE 01/10/2002	CLASS 438	SUBCLASS /	GAU 2818	EXAMINER K. J. S. K.
**APPLICANTS: Matsumura Hideki, Izumi Akira, Masuda Atsushi, Nashimoto Yasunobu, Miyoshi Yosuke, Nomura Shuji, Sakurai Kazuo, Aoshima Shouichi					
**CONTINUING DATA VERIFIED: THIS APPLICATION IS A DIV OF 09/102.665 06/23/1998 PAT 6.349.669 WHICH IS A DIV OF 08/924.304 09/05/1997 PAT 6.069.094					
** FOREIGN APPLICATIONS VERIFIED: JAPAN 8-257675 09/06/1996					
PG-PUB <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>			
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no Verified and Acknowledged Examiners's initials				ATTORNEY DOCKET NO 971003B	
TITLE : Method and apparatus for depositing a thin film, and semiconductor device having a semiconductor-insulator junction					

NOTICE OF ALLOWANCE MAILED		Assistant Examiner	CLAIMS ALLOWED	
			Total Claims	Print Claim for O.G.
ISSUE FEE		Primary Examiner	DRAWING	
Amount Due	Date Paid		Sheets Drwg.	Figs. Drwg.
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE	Application Examiner	
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